Applicant(s)/Patent Under Application/Control No. Reexamination 10/019,059 DALTON, DAMIAN Notice of References Cited Examiner Art Unit Page 1 of 2 2128 Akash Saxena **U.S. PATENT DOCUMENTS Document Number** Date Classification Name Country Code-Number-Kind Code MM-YYYY 05-2001 711/108 US-6,226,710 Melchior, Timothy A. US-5,423,015 06-1995 Chung, David S. F. 711/108 В С US-US-D US-E US-F US-G н US-US-١ US-J US-Κ US-L US-М FOREIGN PATENT DOCUMENTS **Document Number** Date Country Name Classification Country Code-Number-Kind Code MM-YYYY N 0 Ρ Q R S T **NON-PATENT DOCUMENTS** Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) An associative memory approach to parallel logic event-driven simulation; Dalton, D.; CompEuro '92. 'Computer Systems and U Software Engineering', Proceedings.; 4-8 May 1992; Page(s): 341-346□□ NPL). Model of auto associative memory that stores and retrieves data regardless of their orthogonality, randomness or size; Bairaktaris, D.; System Sciences, Proceedings of the Twenty-Third Annual Hawaii Intnl Conf on Volume: i 2-5 Jan 1990 Page(s): 142-150 A general-purpose CMOS associative processor IC and system; Stormon, C.D. et al; Micro, IEEE Volume 12, Issue 6, Dec.

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